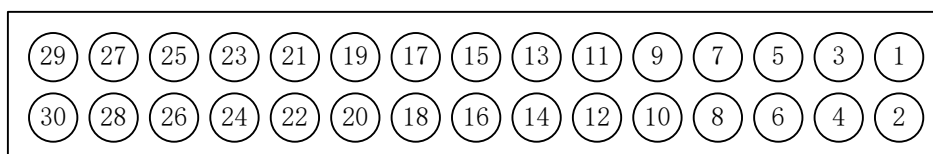


Pin Description

Pin#	Name	I/O	Comment
1	Vcc+3.3V	PWR	Power
2	Vcc+3.3V	PWR	Power
3	GND	GND	Ground
4	GND	GND	Ground
5	XRESET	I	Reset Signal (L: Reset)
6	GND	GND	Ground
7	MCK	I	I2S Master Clock (256fs or 384fs)
8	GND	GND	Ground
9	SDO_SUB	O	I2S Output Data
10	SDI_AUX	I	I2S Input Data
11	SDO_MAIN	O	I2S Output Data
12	SDO_MIC	O	I2S Output Data
13	SDI_MIC	I	I2S Input Data
14	SDI_MUSIC	I	I2S Input Data
15	WCK	O	I2S Word Clock (fs)
16	BCK	O	I2S Bit Clock (64fs)
17	UART_TX	O	UART Transmit Data
18	UART_RX	I	UART Receive Data
19	PEAK_1	O	MIC1 Input Peak Signal
20	PEAK_2	O	MIC2 Input Peak Signal
21	PEAK_3	O	MUSIC Input Peak Signal
22	PEAK_4	O	AUX Input Peak Signal
23	GND	GND	Ground
24	MCK_SEL	I	Master Clock Select (L:256fs, H:384fs)
25	N. C.	---	Not Connect. (Use for TEST)
26	N. C.	---	Not Connect. (Use for TEST)
27	N. C.	---	Not Connect. (Use for TEST)
28	N. C.	---	Not Connect. (Use for TEST)
29	N. C.	---	Not Connect. (Use for TEST)
30	N. C.	---	Not Connect. (Use for TEST)

Pin Configuration



(Top View)